

AMENDMENTS TO CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A bandgap reference circuit for generating a reference voltage, comprising:

an operational amplifier having an output end, first and second input ends respectively connected to an input differential pair of N-type metal oxide semiconductor transistors, and a bias circuit for outputting a bias current to the operational amplifier;

a plurality of transistors connected to the operational amplifier, wherein the first input end is connected to a drain of a first transistor of the plurality of transistors, the second input end is connected to the drain of a second transistor of the plurality of transistors, and the output end is connected to the gates of the plurality of transistors;

a plurality of resistances connected to the plurality of transistors, wherein a first and a second resistances resistance of the plurality of resistances are is connected to said first input end of the operational amplifier, and second and third resistances are connected to said second input end of the operational amplifier, said first and third resistances being matching resistances used for voltage level shifting so that the operational amplifier can normally operate; and

a plurality of bipolar junction transistors connected to the plurality of resistances.

2. (Canceled)

3. (Canceled)

4. (Canceled)

5. (Canceled)

6. (Currently Amended) The bandgap reference circuit of claim 1, wherein supply the reference voltage is less than or equal to 1.5V.

7. (Canceled)

8. (Canceled)

9. (Canceled)